



## Product/Process Change Notice - PCN 17\_0181 Rev. A

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

*Note: Revised fields are indicated by a red field name. See Appendix B for revision history.*

**PCN Title:** Data Sheet Corrections for ADSP-2157x and ADSP-SC57x Products  
**Publication Date:** 31-Jan-2018  
**Effectivity Date:** 31-Jan-2018 *(the earliest date that a customer could expect to receive changed material)*

### Revision Description:

Corrected the date of the Rev A datasheet release to be released by February 28, 2018.

### Description Of Change

The following specification changes will be made to the Rev A Data Sheet:

- 1) Rev 0 page 18 of 142 "• 12-bit ADC core (10-bit accuracy) with built in sample and hold."  
Rev A page 18 of 142 "• 12-bit ADC core with built in sample and hold."
- 2) Rev 0 page 62 of 142 Table 31 DD-Type\_2575 0.50, DD-Peak\_100 1.20  
Rev A page 62 of 142 Table 31 DD-Type\_2575 0.53, DD-Peak\_100 1.27
- 3) Rev 0 page 64 of 142 Table 35 Offset error +-8, Offset error matching +-10  
Rev A page 64 of 142 table 35 Offset error +-5, Offset error matching +-6
- 4) REV 0 - Pg 49 – Table 25- HADC0\_VREFN "Desc: HADC0 Ground Reference for ADC Notes: Can be left floating if HADC and TMU are not used"  
Rev A – Pg 50 – Table 25 - HDACO\_VREFN "Desc: HADC0 Ground Reference for ADC, Notes: Connect to GND if HADC and TMU are not used"
- 5) Rev 0 (Table 25) Px\_nn PORT pins and DAI0\_PIN16 to 01 pins –"Notes: No notes"  
Rev A- (Table 25) Px\_nn PORT pins and DAI0\_PIN16 to 01 pins –" Notes: See note2"  
Note2 "Input by default. When unused, terminate externally in hardware or enable the internal pull-up resistor (when applicable) in software. When present, the internal pull-up design holds the internal path from the pins at the expected logic levels. To pull up the external pads to the expected logic levels, use external resistors."
- 6) Rev 0 (Table 25) Px\_nn PORT pins and DAI0\_PIN16 to 01 pins Internal Termination column is "PullDown"  
Rev A (Table 25) Px\_nn PORT pins and DAI0\_PIN16 to 01 pins Internal Termination column is "Programmable PullUp 1" and note 1 is "Disabled by default."

Changes will be reflected in the Rev A datasheet, available by February 28, 2018.

### Reason For Change

To better align with JEDEC JESD79 specifications and to more accurately represent the functionality of the product.

### Impact of the change (positive or negative) on fit, form, function & reliability

Customers are advised to confirm these changes against their product designs. These changes are not expected to impact the fit, form,

function or reliability of the ADSP-2157x and ADSP-SC57x products.

### Summary of Supporting Information

These corrections will be reflected in Rev A of the product Data Sheet available by February 28, 2018.

Supporting Documents      None

**For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.**

**Americas:**    PCN\_Americas@analog.com

**Europe:**    PCN\_Europe@analog.com

**Japan:**      PCN\_Japan@analog.com

**Rest of Asia:**    PCN\_ROA@analog.com

**Appendix A - Affected ADI Models**

**Existing Parts - Product Family / Model Number (58)**

ADSP-21571 / ADSP-21571BSWZ-4	ADSP-21571 / ADSP-21571BSWZ-5	ADSP-21571 / ADSP-21571CSWZ-4	ADSP-21571 / ADSP-21571CSWZ-5	ADSP-21571 / ADSP-21571KSWZ-4
ADSP-21571 / ADSP-21571KSWZ-5	ADSP-21571W / AD21571WCSWZ400	ADSP-21571W / AD21571WCSWZ500	ADSP-21573 / ADSP-21573BBCZ-4	ADSP-21573 / ADSP-21573BBCZ-5
ADSP-21573 / ADSP-21573CBCZ-4	ADSP-21573 / ADSP-21573CBCZ-5	ADSP-21573 / ADSP-21573KBCZ-4	ADSP-21573 / ADSP-21573KBCZ-5	ADSP-21573W / AD21573WCBCZ400
ADSP-21573W / AD21573WCBCZ500	ADSP-21573W / ADW95168Z-00	ADSP-21573W / ADW95168Z-00RL	ADSP-21573W / ADW95169Z-00	ADSP-21573W / ADW95169Z-00RL
ADSP-SC570 / ADSP-SC570BSWZ-4	ADSP-SC570 / ADSP-SC570BSWZ-42	ADSP-SC570 / ADSP-SC570CSWZ-4	ADSP-SC570 / ADSP-SC570CSWZ-42	ADSP-SC570 / ADSP-SC570KSWZ-4
ADSP-SC570 / ADSP-SC570KSWZ-42	ADSP-SC571 / ADSP-SC571BSWZ-3	ADSP-SC571 / ADSP-SC571BSWZ-4	ADSP-SC571 / ADSP-SC571BSWZ-5	ADSP-SC571 / ADSP-SC571CSWZ-3
ADSP-SC571 / ADSP-SC571CSWZ-4	ADSP-SC571 / ADSP-SC571CSWZ-5	ADSP-SC571 / ADSP-SC571KSWZ-3	ADSP-SC571 / ADSP-SC571KSWZ-4	ADSP-SC571 / ADSP-SC571KSWZ-5
ADSP-SC571W / ADSC571WCSWZ300	ADSP-SC571W / ADSC571WCSWZ400	ADSP-SC571W / ADSC571WCSWZ500	ADSP-SC572 / ADSP-SC572BBCZ-4	ADSP-SC572 / ADSP-SC572BBCZ-42
ADSP-SC572 / ADSP-SC572CBCZ-4	ADSP-SC572 / ADSP-SC572CBCZ-42	ADSP-SC572 / ADSP-SC572KBCZ-4	ADSP-SC572 / ADSP-SC572KBCZ-42	ADSP-SC572W / ADSC572WCBCZ400
ADSP-SC572W / ADSC572WCBCZ4200	ADSP-SC573 / ADSP-SC573BBCZ-3	ADSP-SC573 / ADSP-SC573BBCZ-4	ADSP-SC573 / ADSP-SC573BBCZ-5	ADSP-SC573 / ADSP-SC573CBCZ-3
ADSP-SC573 / ADSP-SC573CBCZ-4	ADSP-SC573 / ADSP-SC573CBCZ-5	ADSP-SC573 / ADSP-SC573KBCZ-3	ADSP-SC573 / ADSP-SC573KBCZ-4	ADSP-SC573 / ADSP-SC573KBCZ-5
ADSP-SC573W / ADSC573WCBCZ300	ADSP-SC573W / ADSC573WCBCZ400	ADSP-SC573W / ADSC573WCBCZ500		

**Appendix B - Revision History**

<b>Rev</b>	<b>Publish Date</b>	<b>Effectivity Date</b>	<b>Rev Description</b>
Rev. -	25-Jan-2018	25-Jan-2018	Initial Release
Rev. A	31-Jan-2018	31-Jan-2018	Corrected the date of the Rev A datasheet release to be released by February 28, 2018.

Analog Devices, Inc.

DocId:4357 Parent DocId:None Layout Rev:7